

A-SSCC 2017 Program Schedule At-a-glance

Date	Time	Convention Center 3F Emerald B	Convention Center 3F Lobby	Main Building 2F Triangle Foyer		Board Room	Peacock		
Monday Nov. 6th 2017	08:30~	On-Site Registration (Convention Center 3F Lobby)					VIP Room	Meeting Room	
	09:00-10:30 (90')	Tutorial 1 ADC hybrids and ADC morphing Michael P. Flynn University of Michigan, USA							
	10:30-10:45 (15')	Break Time							
	10:45-12:15 (90')	Tutorial 2 Accelerator Design for Deep Learning Training Jinwook Oh IBM Research, USA							
	12:15-13:30 (75')	Lunch Break							
	13:30-15:00 (90')	Tutorial 3 Basics of Jitter in Wireline Communications Ali Sheikholeslami University of Toronto, Canada							
	15:00-15:15 (15')	Break Time							
	15:15-16:45 (90')	Tutorial 4 Emerging memory technology for IoT and AI applications Takayuki Kawahara Tokyo University of Science, Katsushika, Japan				SDC Exhibits (16:00-17:00)			
	16:45-17:00 (15')	Break Time							
	17:00-18:00 (60')	SSCS Young Professionals and Students Mentoring /Career Coaching Session							
	18:00-19:30 (90')	Welcome Reception (Main Building 2F Triangle Foyer)							
	19:30-20:00 (30')	TPC Meeting							

Date	Time	Convention (A) Hall	Convention (B) Hall	Convention (C) Hall	Convention (D) Hall	Convention (E) Hall	Grand Ballroom	Convention Center 4F Lobby	Board Room	Peacock Main B/D 2F						
Tuesday Nov. 7th 2017	08:15~	Registration Desk Open							Registration	VIP Room	Meeting Room					
	08:30-08:45 (15')	Opening Ceremony / Welcome Speech (Convention Hall A)														
	08:45-09:30 (45')	Session 1: Plenary Speech 1 (Convention Hall A) Dr. Joseph Yoon <i>(Senior Vice President, Head of Vehicle Component Tech. Center, LG Electronics, Korea)</i>														
	09:30-10:15 (45')	Session 1: Plenary Speech 2 (Convention Hall A) Dr. Prof. Shaojun Wei <i>(Dean of the Dept. of Micro- and Nano-Electronics, Tsinghua University, China Vice president, China Semiconductor Industry Association, China)</i>														
	10:15-10:45 (30')	Break Time														
	10:45-12:50 (125')			Session 2 Low-Power Programmable SoCs and Embedded Memories	Session 3 Circuits and Systems for Sensing and Security										S.C Meeting (11:30~13:30)	
	12:50-13:50 (60')	Lunch Break (Grand Ballroom, Main Building 2F)														
	13:50-15:30 (100')		Session 5 Digital Building Blocks	Session 6 PAM-4 Receiver Techniques	Session 4 Sensor Interface	Session 7 Building Blocks for Frequency Synthesizers							SDC Exhibits (10:50-17:00)			
	15:30-16:00 (30')	Break Time														
	16:00-17:40 (100')	Session 8: Panel Discussion (Convention Hall A)														
	17:40-18:00 (20')	Break Time														
	18:00-20:00 (120')	Banquet (Grand Ballroom, Main Building 2F)														

Date	Time	Convention (A) Hall	Convention (B) Hall	Convention (C) Hall	Convention (D) Hall	Convention (E) Hall	Grand Ballroom	Convention Center 4F Lobby	Board Room	Peacock					
Wednesday Nov. 8th 2017	08:15~	Registration Desk Open							Registration	VIP Room	Meeting Room				
	08:30-09:15 (45')	Session 9: Plenary Speech 3 (Convention Hall A) Dr. Shinsuke Sakakibara <i>(Executive Director & Chief Technical Advisor, Robot Business Division, Fanuc Corporation, Japan)</i>													
	09:15-10:00 (45')	Session 9: Plenary Speech 4 (Convention Hall A) Dr. Kou-Hung Lawrence Loh <i>(Corporate Senior Vice President, MediaTek Inc. President, MediaTek USA Inc, USA)</i>													
	10:00-10:30 (30')	Break Time													
	10:30-12:35 (125')		Session 11 Advanced Imaging System	Session 12 Memory System	Session 10 Power Management	Session 13 Wireless Receivers and Transmitters									
	12:35-13:40 (65')	Lunch Break (Grand Ballroom, Main Building 2F)													
	13:40-15:20 (100')		Session 15 Nyquist-rate ADCs	Session 16 Machine Learning and Recognition SoCs	Session 14 Energy-efficient & Variation- resilient Digital Circuits	Session 17 Advanced Wireline Clock Generators and Transmitters									
	15:20-15:50 (30')	Break Time													
	15:50-17:30 (100')		Session 19 High-resolution ADCs	Session 20 IPs for Emerging Applications	Session 18 Analog Techniques	Session 21 High- performance RF Frequency- generation Techniques									